

Terabit Optical Ethernet and Enabling Integration Technologies

Daniel J. Blumenthal

Terabit Optical Ethernet Center (TOEC), Department of ECE, University of California at Santa Barbara
 danb@ece.ucsb.edu

Abstract: The potential evolution to Terabit Ethernet transmission and network systems will be discussed. Evolution directions and applications drivers as well as transmission, switching and routing technology and photonic integration will be covered.

OCIS codes: Integrated optics: (130.6622), General Fiber optics and optical communications: 060.0060, Fiber optics and optical communications: 060.2360 Fiber optics links and subsystems

1. Introduction

Today's communications and data networks are pushing capacities that will drive the 40Gbps and 100Gps Ethernet markets to be ubiquitous. Ethernet has become the de facto standard for all future applications including voice, video, data storage, and more recently cloud computing, data centers, and large scale high bandwidth FTTH deployment. At one extreme, pervasive applications like the Intel fiber to the chip for PC applications standard will drive fiber to the laptop with 100Gbps Ethernet and beyond [1]. At the other extreme large-scale applications like cloud computing will push unheard of boundaries for computing and communications requirements as evidenced by projections that cloud computing will exceed YottaBytes in 5-10 years. Ethernet is the most widely installed LAN and DC technology and is becoming pervasive in the Enterprise, WAN and LH transport, reducing costs of transport protocols like SONET. The explosive growth number of low bandwidth IP connected edge devices coupled with new anticipated growth in high definition TV and cloud computing/distributed data centers will continue to drive bandwidth requirements past today's 100GbE.

Ethernet was originally developed from Aloha-Net as a random media access network protocol designed to run with many users asynchronously accessing the same network. For Ethernet data rates lower than 10Gbps, the protocol is designed to run asynchronous using techniques like collision detection and avoidance to handle the random media access. However, as Ethernet become more pervasive (over alternative protocols like ATM) and evolved to a high-speed transport protocol at 10-Gigabit per second and beyond, media access with collision detection was set aside, and only the low cost transport features remained in the protocols to reduce the cost of transport over more traditional SONET protocols [2]. As a transport protocol, framing was introduced into originally variable sized asynchronous packet communications in order to facilitate transmission requirements over fiber media. The 10Gbps Ethernet approach was extended to 100GbE [5]. At these faster data rates new modulation techniques were employed to overcome fiber transmission impairments for medium to long haul distances.

The 100G standardization has ranged from 10x10Gbps and 4x25Gbps to serial 100Gbps depending on the application, with the serial standard just finalized and designed to be carried over a standardized optical transport channel. As discussed in [2], [5] and [3], the first 100GE standards were in place for short range applications like data centers and LAN Ethernet Transport, utilizing current 10GbE technology with 10 wavelengths or four wavelengths with 25-Gb/s technology. Operation at 1300 enables a migration to 100GbE and 400GbE using 16 x 25Gbps with the potential for evolution to 40Gbps external modulated laser designs. Near term barriers to operating at 400Gbps using 4x100Gbps for DC and LAN applications will most likely occur due to cost of 100GE. For longer reach applications, the lower dispersion tolerance of 100G transmission over 25G transmission [3] and nonlinear distortion resulting from higher required launched powers will challenge deployment to 400G and 1 Terabit.

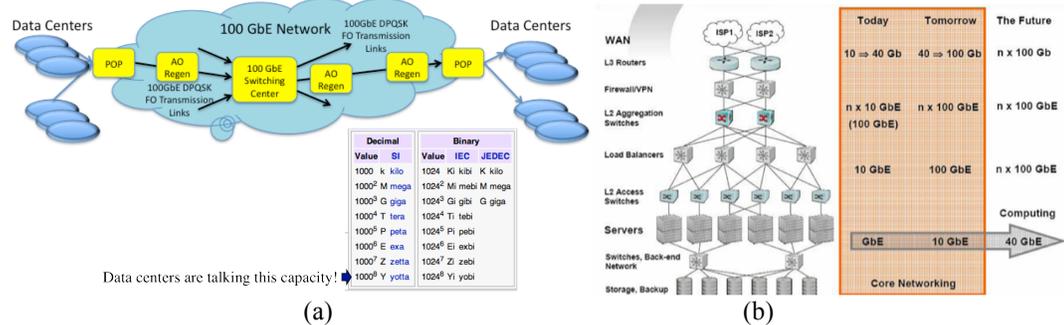


Figure 1.

2. New Applications Drivers

The drive to 1TbE will most likely come initially from new data intensive applications like data centers [9], communications between data centers [8] or new high volume high data rate applications like video distribution [7][4]. Increases in IP traffic growth due in particular to consumer demand for services like video are projected to increase by a factor of 5-fold by 2013 [7]. As shown in Figure 2a, today's 100GbE links will be used to interconnect data centers with 100GbE used mostly as a transmission technology. However interconnected data centers will be required to support cloud computing environments and new global data center applications. New functions at 100GbE like line regeneration and higher bandwidth Ethernet switching and routing will be required in the near term with link data rates growing to 400G and beyond. Figure 2b shows the traditional data center communications, computation and storage hierarchy and Ethernet growth to higher multiples of 100GbE.

4. Moving to Terabit Ethernet

True Ethernet is more than just transport, it includes many functions including *(1) Aggregation, (2) Synchronization, (3) Transmission, (4) Transport, (5) Switching and Forwarding (6) ADC and (7) Electronic Processing*. In LAN and DC environments, even as single channel Ethernet rates climb to 40G per channel, functions 1-7 will be required at wire rates by traditional electronic router chip sets. Power, cost and footprint challenges will continue to emerge as Ethernet functions have to be handled at 40GE, 100GE, 400GE and TbE wire rates and will push the practical limits of systems built on pure electronic solutions. Historical growth of high-end router capacity and the dependence on continued electronics performance to maintain flat system power footprint will drive re-architecting routers and data networks [7]. The system bandwidth of high capacity systems and even edge systems in the data center and external telecommunications infrastructure will rely on continued improvements in electronics in terms of MHz-gate/mW and Mbps/W in order to keep the system power flat while scaling capacity.

Transmission: While parallel transmission will most likely be the near term economic choice for DC and LAN applications, the economics of moving to single carrier/channel 100GbE and beyond in the MAN, WAN and longer distance transmission will continue with increased transmission speed. Historically, we have seen the move to 10GE, 40GE and now 100GE for reasons of more efficient use of wire-speed switching fabrics, better economics for higher speed interfaces and the ability for a single high capacity channel to handle peak bandwidth and lower operations and maintenance costs (OAM) over parallel wavelength or channel transmission Ethernet links [6]. In terms of raw transmission there is a wealth of work underway on Terabit modulation formats, improved spectral efficiency, transmission reach and other issues required for practical Terabit transmission. Since tradeoffs exist between transmission reach vs. spectral efficiency and between modulation symbol-space vs. SNR, it is an open question if single-carrier or multi-carrier systems will be used to transport Terabit Ethernet [16]. Examples of single channel Terabit transmission experiments to date include [11], [12] and [13]. Examples of multi-carrier include OTDM and Superchannels [14]. Pushing for single channel 400G transmission for WAN distances and beyond could be achieved using techniques with 8-b/s/Hz spectral efficiency to 16 b/s/Hz [3]. Recent studies on the Shannon capacity of nonlinear fiber channels estimate the spectral efficiency limit of a polarization-multiplexed 1000-km link of standard fiber to be at ~ 16 b/s/Hz [24]. At ECOC 2010, Ericsson projected next generation Ethernet to run at 400Gbps PM-16QAM single-carrier in 2015 and 1TB commercial in 2018 commercial with required spectral efficiencies of 8b/Hz. The practical limits of transmission for high spectral efficiency channels include fiber nonlinear distortion of signal constellations [21]. A key function to enable Terabit coherent transmission is the optical coherent 3R regenerator, to overcome the dispersion, nonlinear and ASE induced transmission impairments. Research on 3R all-optical regeneration for coherent systems shows promise for lower cost repeaters with tolerance to nonlinearities, ASE and dispersion is underway and examples of published results include [24], [25], [22], [23].

Another key issue is related to if Terabit Ethernet will remain a framed optical transport protocol or if there are new technology opportunities to move to a media access based protocol so that more of the functions 1-7 can be handled in addition to transport. This could become increasingly important as the power required to switch and regenerate Terabit Ethernet channels could become prohibitively expensive and power hungry if done using traditional lower speed electronic techniques. The architecting of distributed networks for applications like cloud computing could put pressure on having the ability to add/drop and route packets at Terabit rates.

DSPs and ADCs: The edge where electronics is used for converting coherent optical analog signals to electronics signals and for compensation of fiber impairments will hit barriers due to practical in-line Terabit electronics. In order to move to single channel or multi-channel Terabit Ethernet, the amount of ADC and DSP will most likely become prohibitive in cost and power consumption. Among the open issues are optical vs. electrical dispersion compensation and ADC and the electronics bandwidth for high symbol constellations at high modulation rates. For example, the 56G electrical bandwidth for PM 16 QAM, yet will require multi-terabit DSP processing capacity! The impact on high ADC sampling rate > 100 Gsps, with the required sampling resolutions have been discussed in a paper on limits and it is likely that current technologies will run out of steam for Terabit Ethernet

[17]. Optical ADC is a potential alternative that can significantly reduce the power requirements for a given bandwidth, bit resolution and dynamic range. Examples include [26] [18], [19] and [20].

Synchronization, Switching, Buffering and Routing: At the higher levels of functions 1-7, optical packet synchronization and buffering for statistical multiplexing/demultiplexing [33][35][36][37][38], switches and add/drop multiplexers that can support Terabit rates [27][28][29][30][31], and edge multiplexers for speeding up lower bit rate packets to Terabit rates [34] will be required if true Terabit Ethernet is to be performed at all levels of the Ethernet hierarchy. To date, many of the experimental implementations have been based on nonlinear fiber effects with some results reported using semiconductor electroabsorption modulators (EAM). Packet routing has been demonstrated out to 160Gbps packets [32], and these techniques are scalable to Terabit rates.

5. Photonic Integration

Key to all aspects of moving to Terabit Ethernet is photonic integrated circuits (PICs). The required stability and performance for high modulation symbol space and high bit rates will inherently depend on PIC technology married with low power electronics and lower power photonics are a potential solution [43]. Terabit coherent transmitters and receivers will require new levels of integration density that go way beyond today's state of the art [39] [40] [41] and will require integration of new ultra low loss waveguides [42] and highly power efficient materials and devices to achieve the performance and density required. PICs that can handle higher level functions like switching, regeneration, add/drop and packet forwarding functions

4. References

- [1] Light Peak Technology: <http://www.intel.com/go/lightpeak/>
- [2] John D'Ambrosia, et. al., "40 Gigabit Ethernet and 100 Gigabit Ethernet Technology Overview," [http://www.ethernetalliance.org/files/static_page_files/83AB2F43-C299-B906-8E773A01DD8E3A04/40G_100G_Tech_overview\(2\).pdf](http://www.ethernetalliance.org/files/static_page_files/83AB2F43-C299-B906-8E773A01DD8E3A04/40G_100G_Tech_overview(2).pdf), November, 2008.
- [3] P. Winzer, "Beyond 100g Ethernet," IEEE Communications Magazine, pp. 26-30, July 2010
- [4] T. J. Xia, "100Gbps Capacity and Beyond," Private Communication, 2009/2010
- [5] J. D'Ambrosia, "100 Gigabit Ethernet And Beyond," IEEE Comm. Mag., vol. 48, no. 3, pp. S6-S13, March 2010.
- [6] T. Breach, "40G and 100G Overview," Network Architectural Workshop in Brussels - March 31st 2009
- [7] G. Epps, Private communication, Cisco, Inc. 2010.
- [8] C. F. Lam, et al, "Fiber Optic Communication Technologies - what's needed ...," IEEE Optical Communications, July 2010
- [9] H. Liu, et. al., "Scaling Optical Interconnects in Datacenter Networks ...," 2010 18th IEEE Symposium on HPI, pp. 113 - 116.
- [10] R. Freund, et. al., "Single and Multi-Carrier Techniques to Build Terabit/s ...," ECOC 2010 Workshop, Paper WS6, Torino, Italy, 2010.
- [11] H.G. Weber et al, Electron Lett. Vol. 42 No. 3 (2006) 2.56 Tbit/s (640 Gbaud) 160 km.
- [12] Hu et al, "1.28 Tbit/s DPSKH," ECOC 2010
- [13] C. Schmidt-Langhorst, et. al., "Terabit/s Single-Carrier Transmission Systems Based ...," TUD, Lyngby, Sept. 16 - 17, 2010
- [14] S. Chandrasekha", et. al., Terabit Superchannels for High Spectral Efficiency Transmission," TUD, Lyngby, Sept. 16 - 17, 2010.
- [15] A. Carena, et. al., "Coherent Polarization-Multiplexed Formats: Receiver Requirements and ...," ECOC 2010, M.O.2.C.1, Italy, 2010.
- [16] M. Camera, et. al., "Beyond 100G: System Implications towards 400G and 1T," ECOC Symposia S6 Towards 1000Gb/s, Italy, 2010.
- [17] R.H. Walden, "Electronic limits: Analog-to-Digital Converters and Associated IC Technologies," IEEE Comm Mag, Feb 1999.
- [18] T. Sakamoto, et al., ECOC 2007, Post deadline 2.8
- [19] J. Leuthold, et. al., International Symposium on Ultra-high Capacity Optical Communication and ...," TUD, Lyngby, Sept. 16 - 17, 2010
- [20] L. Rau, et. al., "Analog Performance of an Ultrafast Sampled-Time All-Optical Fiber XPM WC," IEEE PTL, April (2003).
- [21] E. Ip, et. al., "Compensation of Dispersion and Nonlinear Impairments Using Digital Back Propagation
- [22] K. Nguyen, et. al., "All-Optical 2R Regeneration of BPSK and QPSK Data using a 90° Optical Hybrid and Integrated SOA-MZI Wavelength Converter Pairs," to be presented at OFC 2011, paper OMT3
- [23] T. Kise "Demonstration of Cascadability and Phase Regeneration of SOA-Based All-Optical DPSK Wavelength Converters," to be presented at OFC 2011, paper OThY3
- [24] R. Slavik et al. "All-optical phase and amplitude regenerator for next-generation telecommunications systems," Nature Photonics, 2010.
- [25] F. Parmigiani et al. "All-optical phase and amplitude regeneration properties of a 40 Gbit/s DPSK ..." ECOC 2010 Paper Mo.2.A.1
- [26] D. J. Blumenthal, "Optical Signal Processing for Optical Packet Switching Networks," IEEE Comm. Mag., 523 - 529, February (2003)
- [27] B. E. Olsson, et. al., "All-Optical Demultiplexing Using Fiber Cross-Phase Modulation (XPM) and ...," IEEE PTL, 875-7, August (2001).
- [28] L. Rau, et. al., "Simultaneous All-Optical Demultiplexing of a 40-Gb/s signal to 4x10 Gb/s ...," IEEE PTL, 1725-7, December (2002).
- [29] H. F. Chou, et. al., "Compact 160-Gb/s Demultiplexer Using a Single-Stage Electrically ...," IEEE PTL, 15(10): 1458-60, October (2003).
- [30] L. Rau. Et. al, "High-speed optical time-division-multiplexed/WDM networks and their ...," JON,100-18, February (2004).
- [31] H-F Chou, et. al. "Compact 160-Gb/s Add-Drop Multiplexer With a 40-Gb/s Base Rate ..." IEEE PTL, 16(6): 1564-6, June (2004).
- [32] W. Wang, et. al., "160 Gb/s variable length packet 10 Gb/s-label all-optical label ...," IEEE JLT, 23(1): 211-8, January (2005).
- [33] R. Salem et al, "Optical time lens based on four-wave mixing on a silicon chip," Opt. Lett. 33, 1047-1049 (2008).
- [34] S. Rangarajan, et. al. "All-Optical Packet Compression of Variable Length Packets From 40 ...," IEEE PTL, 18(2): 322-4, January (2006).
- [35] E. F. Burmeister, et. al., "A comparison of optical buffering technologies," Optical Switching and Networking, 5(1): 10-18, March (2008).
- [36] J. P. Mack, et. al., "Variable length optical packet synchronizer," IEEE PTL, 20(14): 1252-4, July 15 (2008).
- [37] J.P. Mack, et al., "Synchronously loaded optical packet buffer," IEEE PTL, 20(21): 1757 - 9, November 1 (2008).
- [38] N. Behesti et. al., "Optical Packet Buffers for Backbone Internet Routers," IEEE/ACM Transactions on Networking (2010)
- [39] S. C. Nicholes, et. al., "An 8x8 InP Monolithic Tunable Optical Router (MOTOR) Packet Forwarding Chip," IEEE JLT, , (2009) (Invited).
- [40] S. C. Nicholes, "Large-Scale Photonic Integration for Advanced All-Optical Routing Functions," IPR, Monterey, CA, July 25-28 (2010)
- [41] R. Nagarajan et al., "Large-scale photonic integrated circuits for long-haul transmission ...," JON, vol. 6, pp. 102-111, Feb. 2007.
- [42] J. F. Bauters, et. al., "Ultra-low Loss Silica-based Waveguides with Millimeter Bend Radius," ECOC 2010.
- [43] D. J. Blumenthal, et. al, "Integrated Photonics for Low Power Packet Networking,"